

FIG. 1

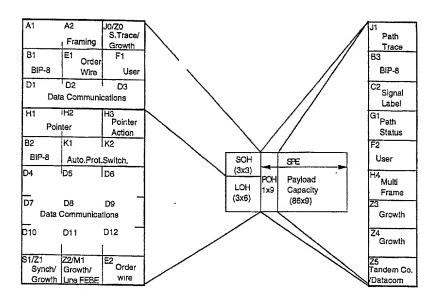
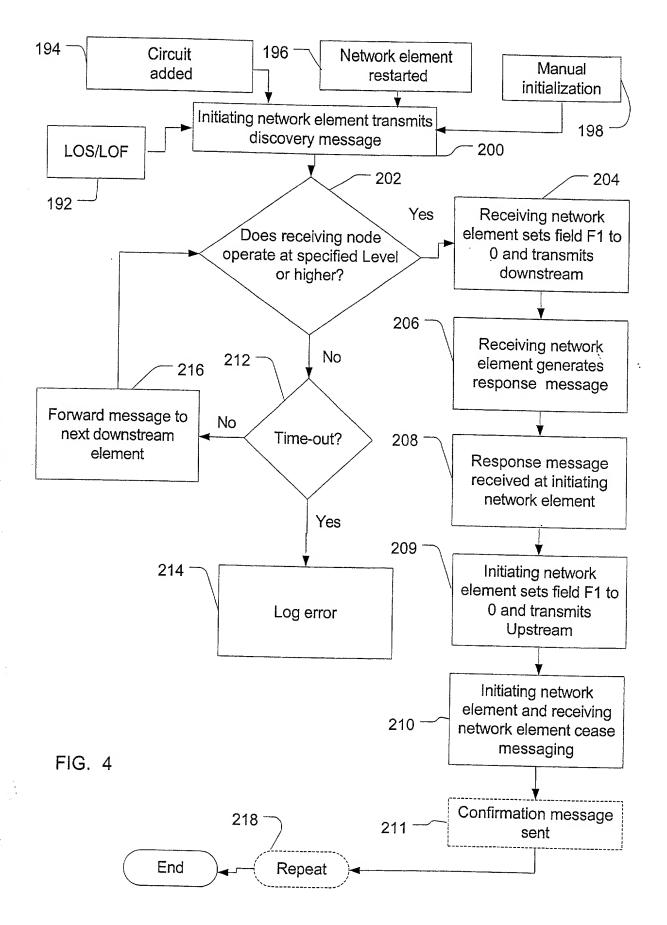


FIG. 2 Conventional Art

0	-	7	ю	4	S.	9	7	∞	6	10	11	12	13	14	15
	Т	I	×	×	×	×	×	×	×	×	P1	P2	P3	P4	P5
	CRC Level DisId	DisId			<i>[</i> 4	Node Identifier	entifier					Por	Port Identifier	fier	
	F1	F2				F3	3						F4		

FIG. 3



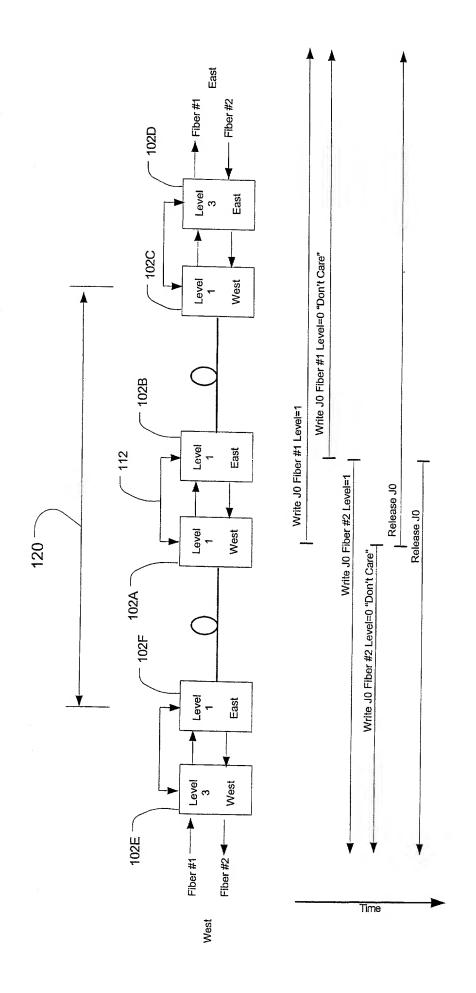


FIG. 5A

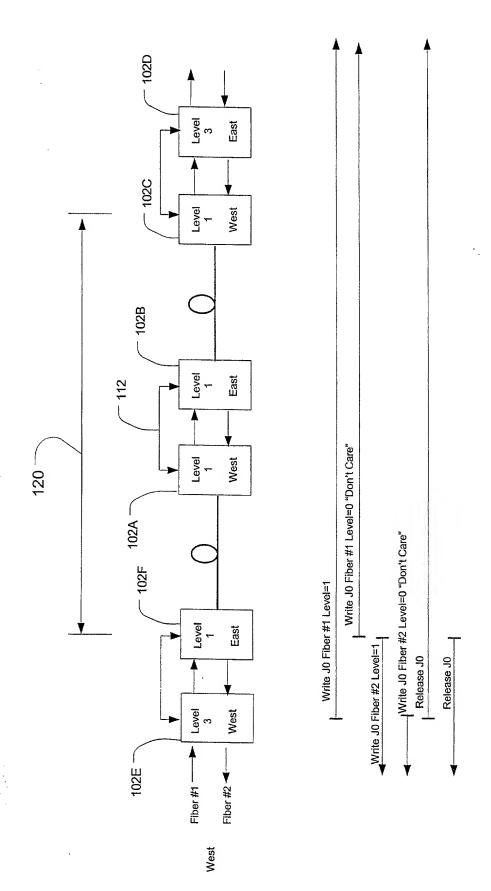


FIG. 5B

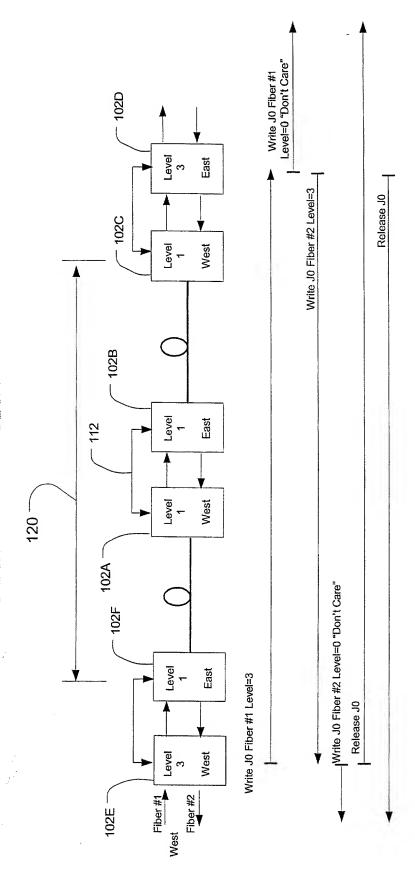
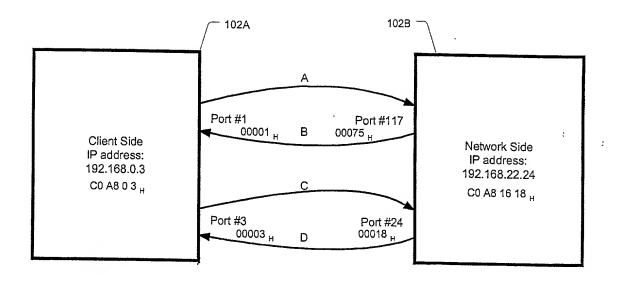
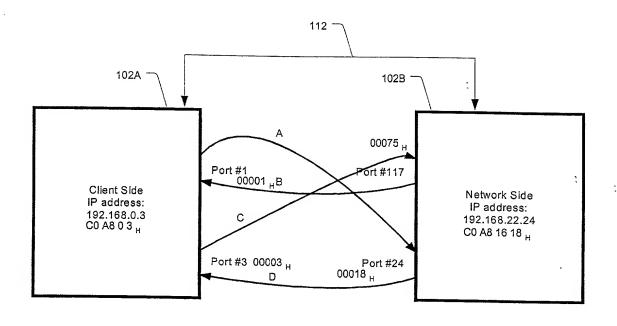


FIG. 5C



- A 1 P C0A80003 00001
- B 1 P C0A81618 00075
- C 1 P C0A80003 00003
- D 1 P C0A81618 00018

FIG. 6



- A 1 P C0A80003 00001
- B 1 P C0A81618 00075
- C 1 P C0A80003 00003
- D 1 P C0A81618 00075

FIG. 7

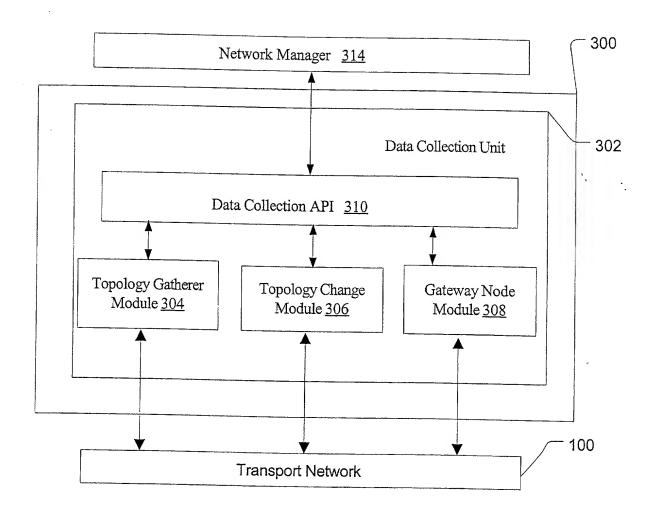


FIG. 8

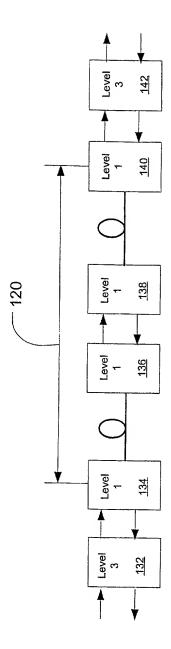
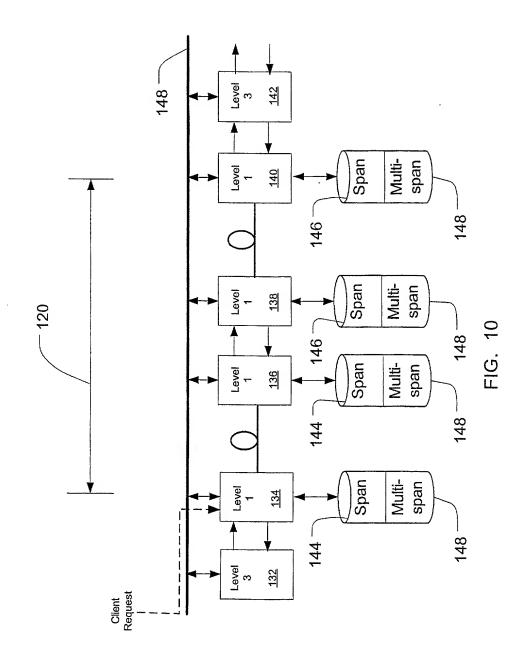
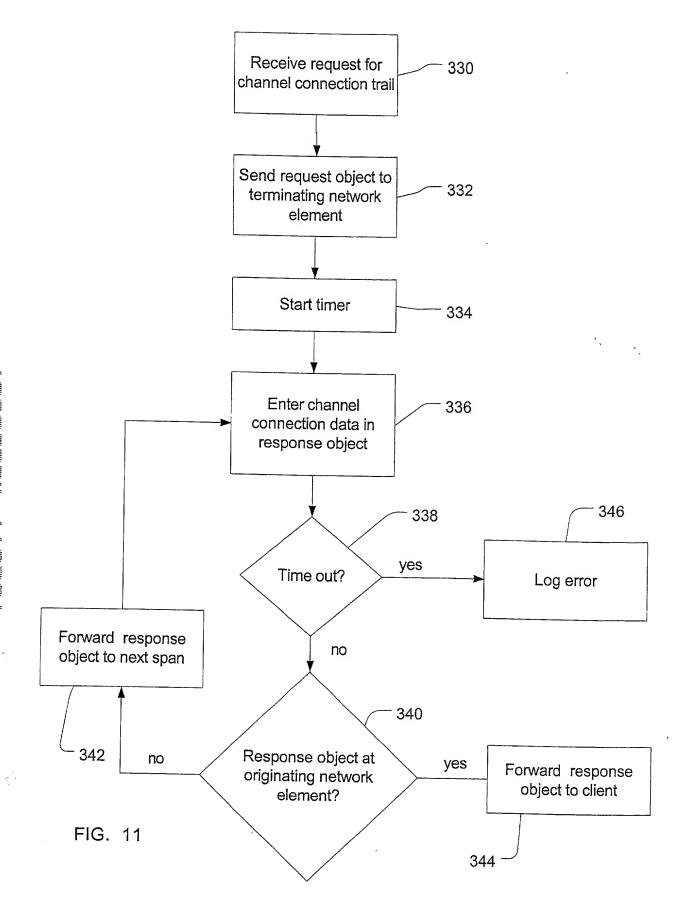


FIG. 9





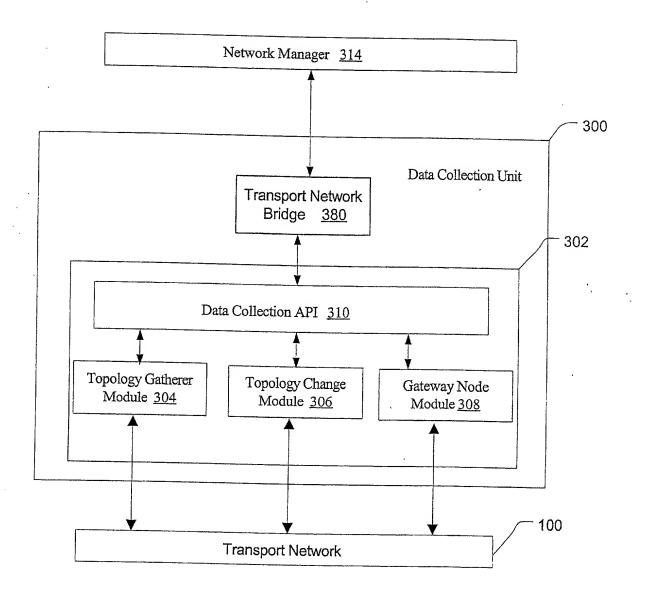


FIG. 12

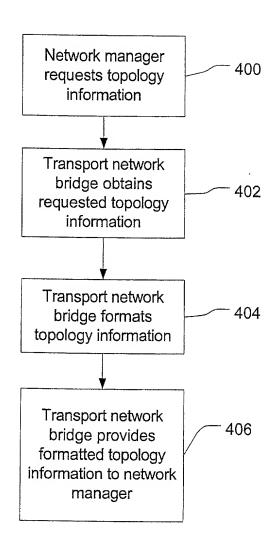


FIG. 13

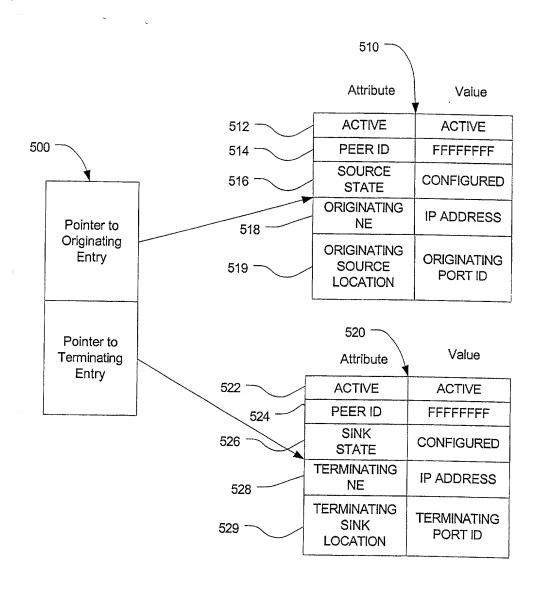


FIG. 14

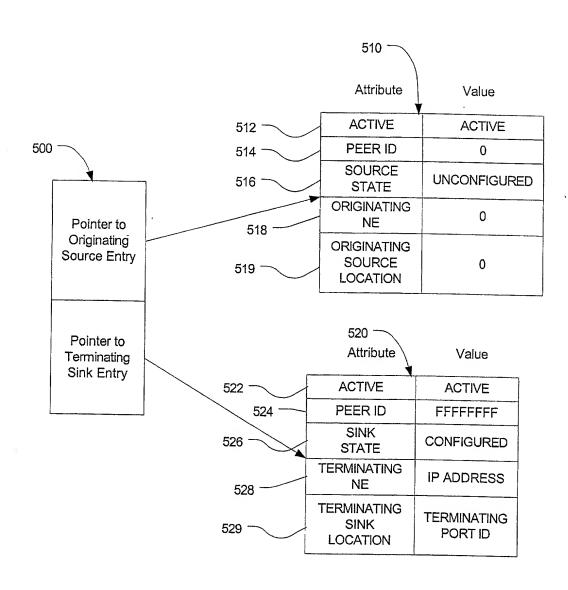


FIG. 15

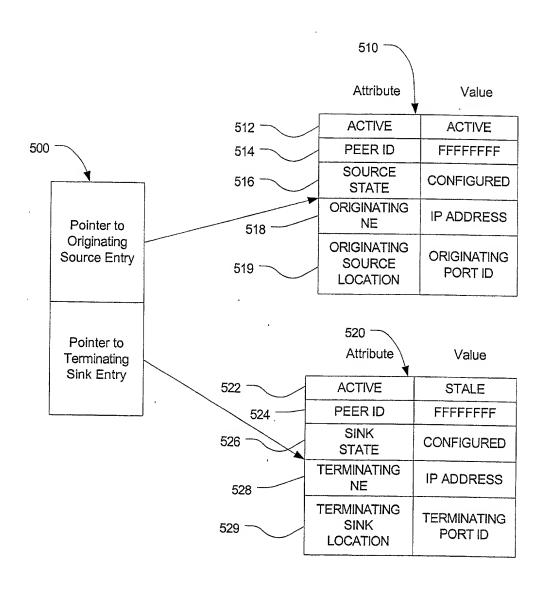


FIG. 16

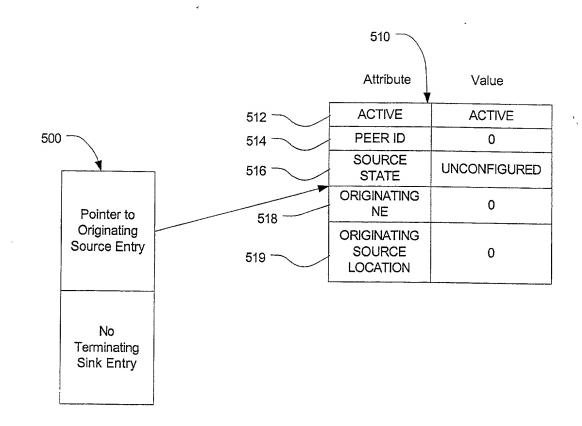


FIG. 17